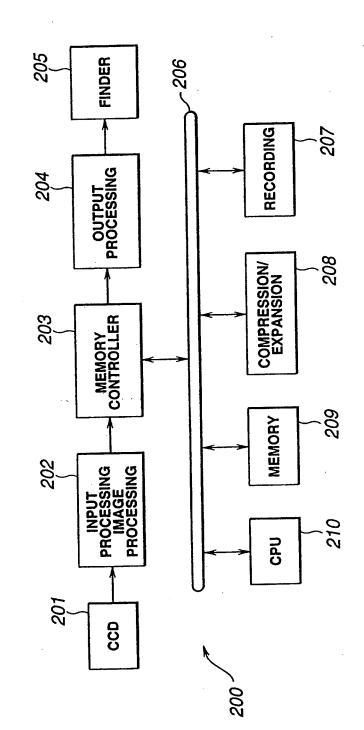


Title: IMAGING APPARATUS IN WHICH REDUCED RESOLUTION IMAGE DATA IS GENERATED, AND WHEN SUPPLIED OVER AN IMAGE DATA BUS, THE RESOLUTION IS INCREASED AND OUTPUTTED Serial No. 09/353,287



**FIG.1** (PRIOR ART)



RECORDING DEVICE  $\sim$ 25 Y/C or VIDE OUT 6 NTSC/PAL ENCODER 23 FLASH MEMORY I/F D/A SUPPLIED OVER AN IMAGE DATA BUS, THE RESOLUTION ,28 RESOLUTION IMAGE DATA IS GENERATED, AND WHEN 43 22 ~30 RESOLUTION Title: IMAGING APPARATUS IN WHICH REDUCED JPEG ROM JPEG //F MEMORY CONTROLLER CPU IS INCREASED AND OUTPUTTED Inventors: Masayuki Takezawa et al. MEMORY I/F IMAGE MEMORY DRAM HOST I/F 31 Serial No. 09/353,287 27 INPUT PROCESSING 26 <u>7</u>₹ SG 2 400 20 COMMUNICATION ₽₽ 5 CC 52

5

**FIG.2** 



Title: IMAGING APPARATUS IN WHICH REDUCED RESOLUTION IMAGE DATA IS GENERATED, AND WHEN SUPPLIED OVER AN IMAGE DATA BUS, THE RESOLUTION IS INCREASED AND OUTPUTTED Serial No. 09/353,287

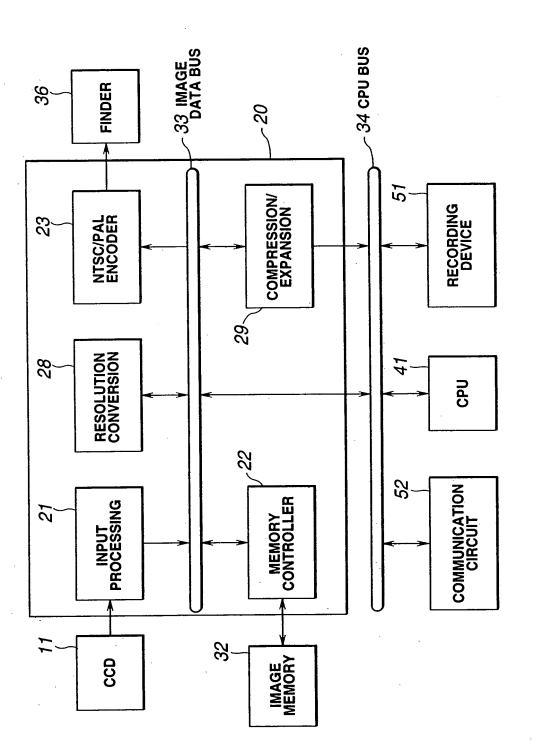


FIG.3



Title: IMAGING APPARATUS IN WHICH REDUCED
RESOLUTION IMAGE DATA IS GENERATED, AND WHEN
SUPPLIED OVER AN IMAGE DATA BUS, THE RESOLUTION
IS INCREASED AND OUTPUTTED
Serial No. 09/353,287

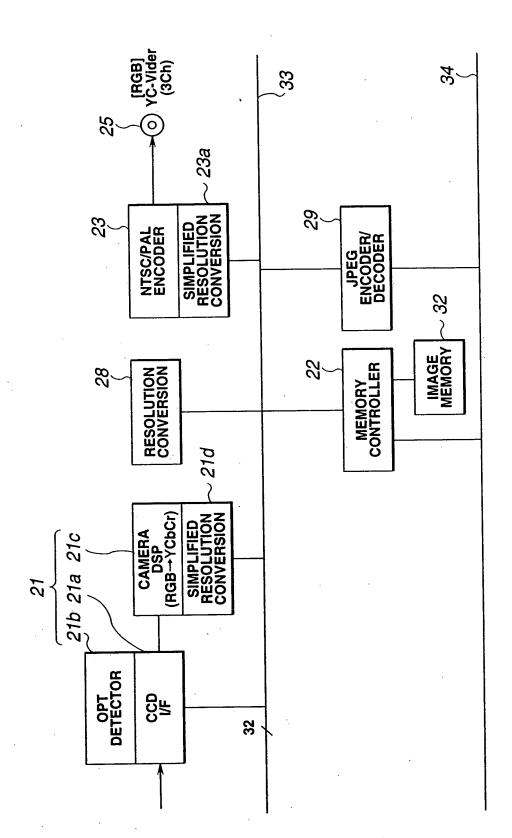


FIG.4



Title: IMAGING APPARATUS IN WHICH REDUCED
RESOLUTION IMAGE DATA IS GENERATED, AND WHEN
SUPPLIED OVER AN IMAGE DATA BUS, THE RESOLUTION
IS INCREASED AND OUTPUTTED
Serial No. 09/353,287

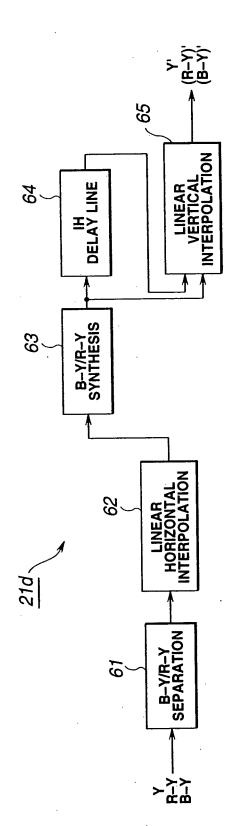
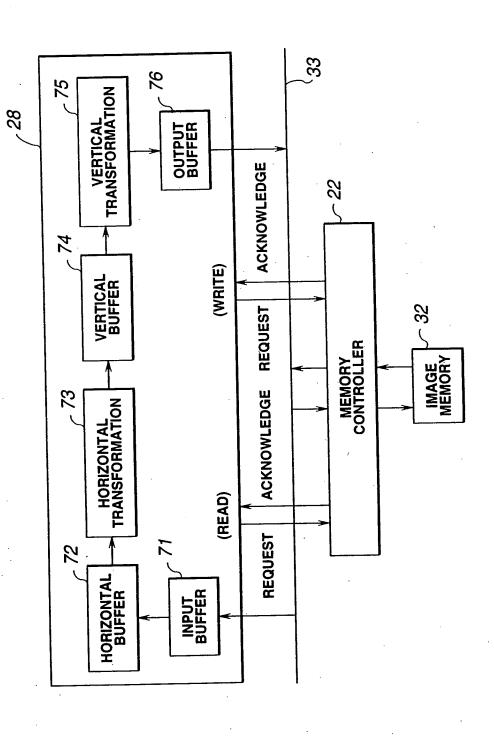


FIG.5



Title: IMAGING APPARATUS IN WHICH REDUCED
RESOLUTION IMAGE DATA IS GENERATED, AND WHEN
SUPPLIED OVER AN IMAGE DATA BUS, THE RESOLUTION
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Serial No. 09/353,287

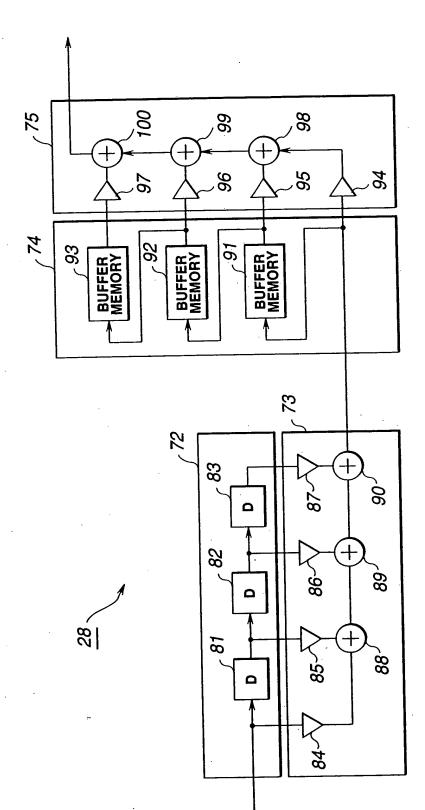


FIG.7



Inventors: Masayuki Takezawa et al.
Title: IMAGING APPARATUS IN WHICH REDUCED
RESOLUTION IMAGE DATA IS GENERATED, AND WHEN
SUPPLIED OVER AN IMAGE DATA BUS, THE RESOLUTION
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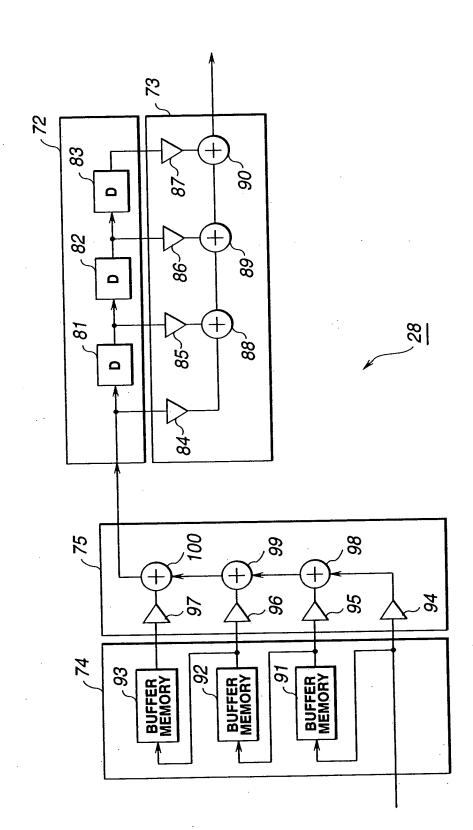


FIG.8



Inventors: Masayuki Takezawa et al.
Title: IMAGING APPARATUS IN WHICH REDUCED
RESOLUTION IMAGE DATA IS GENERATED, AND WHEN
SUPPLIED OVER AN IMAGE DATA BUS, THE RESOLUTION

IS INCREASED AND OUTPUTTED

Serial No. 09/353,287

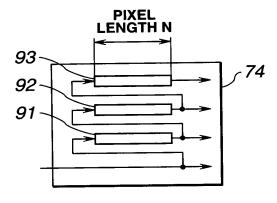
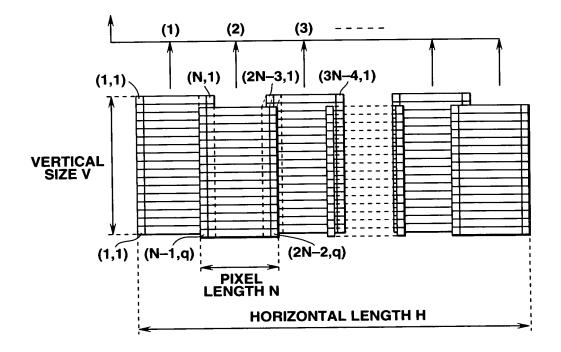


FIG.9

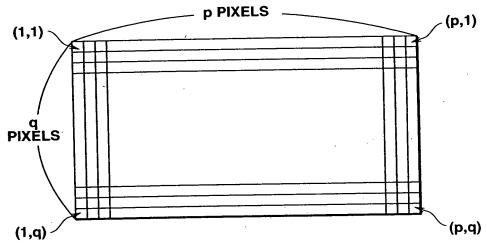


**FIG.10** 

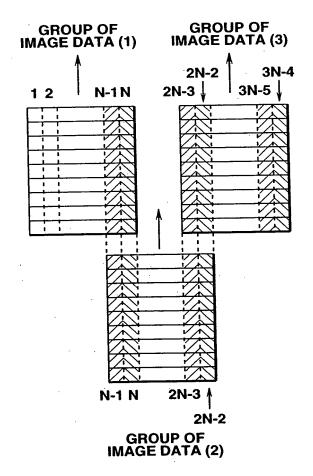


Inventors: Masayukı Takezawa et al. Title: IMAGING APPARATUS IN WHICH REDUCED RESOLUTION IMAGE DATA IS GENERATED, AND WHEN SUPPLIED OVER AN IMAGE DATA BUS, THE RESOLUTION IS INCREASED AND OUTPUTTED

Serial No. 09/353,287



**FIG.11** 

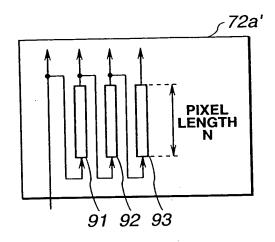


**FIG.12** 

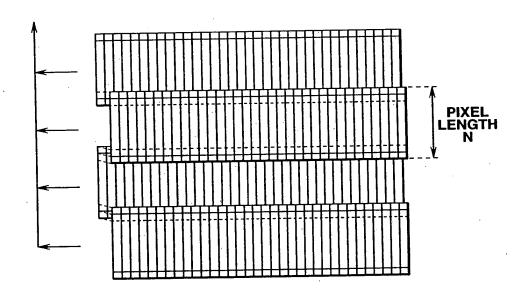


Inventors: Masayuki Takezawa et al.
Title: IMAGING APPARATUS IN WHICH REDUCED
RESOLUTION IMAGE DATA IS GENERATED, AND WHEN
SUPPLIED OVER AN IMAGE DATA BUS, THE RESOLUTION
IS INCREASED AND OUTPUTTED

Serial No. 09/353,287



**FIG.13** 

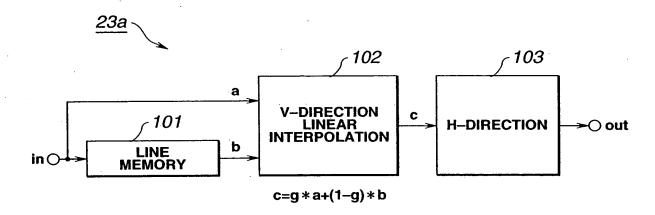


**FIG.14** 

JUN 2 1 2003 CU

Inventors: Masayuki Takezawa et al.

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**FIG.15** 

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Serial No. 09/353,287



ပ ဌ ပ် ဌ င် ဌ ပ် ဌ ဖ ပ် Ŋ ဌ ပ် က ဌ N ට ප 0 ပ် ප 9 ပ် S ප 4 င် က СP 2 ර Сb 1/3 THINNING FIG. 16C CAMDSP PIG. 16C -DRAM

cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb|cr|cb က ဖ S က N Ŋ ဖ FIG.16D DRAM→

Z 1 CAMDSP-DRAM Q+D N+O DRAM→ OSD D→N C→D D→O DRAM→ NTSC N-O Q+O FIG. 16F IBUS— (16bit)

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